

Specification of the allowable current (ripple)

There is no specification in terms of allowable current (ripple) for monolithic ceramic capacitors. However, please carefully follow the points below, and confirm them in the actual circuit before use.

1. The temperature rise (ΔT) caused by the ripple current should not exceed 20 °C above the ambient temperature of the capacitor before power is applied.
2. V_{p-p} value and V_{o-p} value including DC bias for the applied voltage must be within the rated voltage (DC).
3. The maximum operating temperature, including the amount of self-generated heat should not be exceeded.

Please refer to [the appropriate technical data sheet](#) for the self-heating data, from the sine wave ripple current, as reference data. Please also utilize [the software tool](#) (Murata Chip Capacitor Characteristics Data Library) to obtain reference values for circuit design simulation.