

Product Search Data Sheet

Note: This datasheet may be out of date. Please download the latest datasheet of CFWLA455KD1Y-B0 from the official website of Murata Manufacturing Co., Ltd.

http://www.murata.com/en-sg/products/productdetail?partno=CFWLA455KD1Y-B0

CFWLA455KD1Y-B0









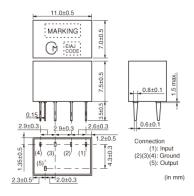
Applications

Unsuitable	Please be sure to read and comply with
Applications	these "Precautions for use."
	Consumer equipment
	Please refer to Our Website and
	specifications, etc. for information about
Specific	the performance, functions, quality,
Applications	management, and safety required for
	the above applications, and use
	Products after confirming the
	performance and reliability of the actual
	Product.



Appearance & Shape







Packaging Information

Packaging	Specifications	Standard Packing Quantity
-B0	Bulk	150



Features

CFWLA_Y series are high selectivity ceramic filters, which consist of 6 ceramic elements connected in a ladder form. They are most suitable for digital communications and mobile telephones because of their improved GDT characteristics.

Features

- 1. High selectivity
- 2. Avariety of bandwidths are available
- 3. Excellent GDT characteristics are available within pass
- 4. Easily mounted on a printed circuit board
- 5. Operating temperature range: -20 to +80 (degrees C), Storage temperature range: -40 to +85 (degrees C)

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Therefore, please review our product specifications or consult the approval sheet for product specifications before ordering





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Specifications

Operating Temperature Range	-20°C to 80°C
Shape	Lead
Elements	6
Center Frequency	455.0kHz
Center Frequency Tolerance	±1kHz
Nominal Center Value	No
6dB Bandwidth	fn±10.0kHzmin.
Stop Bandwidth	25.0kHz
Area of Stop Bandwidth	[within 50dB]
Stop Band Attenuation	40dB[within fn±100kHz]
Insertion Loss	8.0dB[at minimum loss point]
Ripple	1.0dB[within fn±7kHz]
GDT Deviation	30.0µs[within fn±7kHz]
Input/Output Impedance	1500Ω
Mass	690mg

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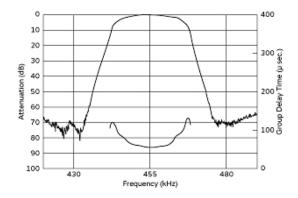
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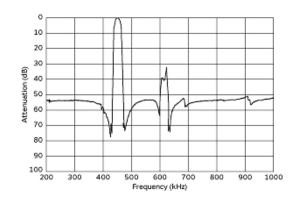
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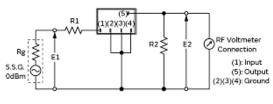
Product Data





Frequency Characteristics

Spurious Response



Rg+R1=R2=Input/Output Impedance

Measurement Circuit

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