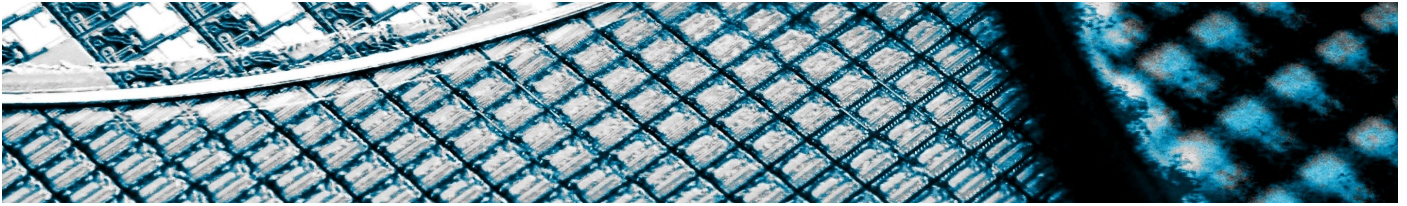
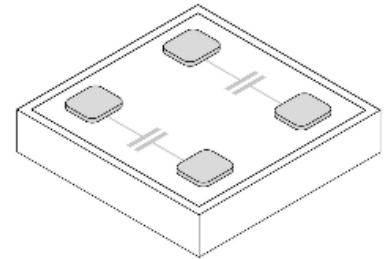


Product Brief



XBDC 2x10nF 0202M WV3.8



Application:
Differential Line Pairing

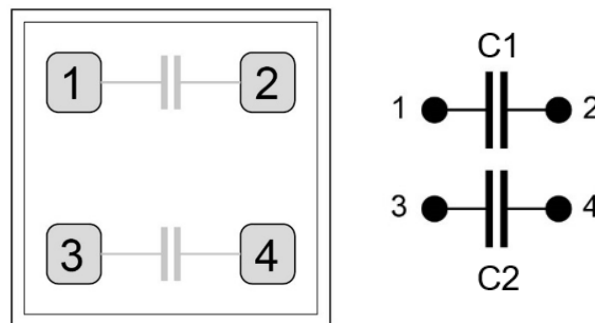
Part Number structure:

9	3	9	3	0	4	4	2	2	5	1	0	-	T	4	N
															S

Product data:

Cap Value	Dimensions			Typical Operating Voltage	Temp. Range	Assembly	Finishing	IL	RL
	L	W	T						
2x10	0.5	0.5	100	3.8 ⁽¹⁾	-55 +150	Surface Mounted	ENIG	0.80 ⁽²⁾	20 ⁽²⁾
nF	mm	mm	μm	V	°C		SAC 305		

(1): 10 years of intrinsic lifetime prediction at 100°C continuous operation
(2): Simulated on microstrip differential transmission line @160GHz @25°C

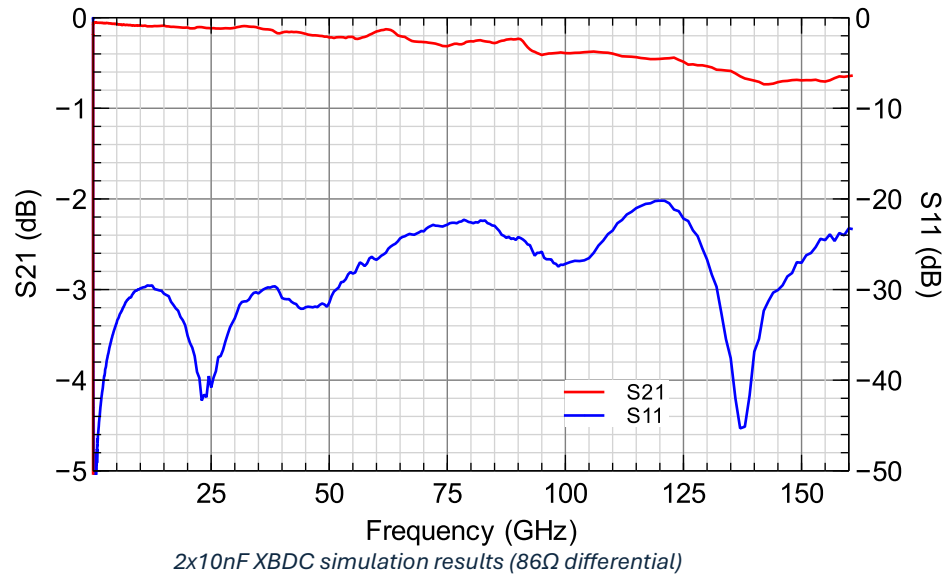


The XBDC family

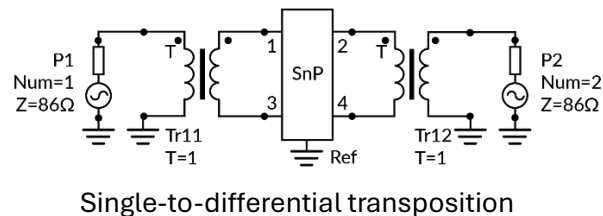
The XBDC is a differential Silicon Capacitor pair designed for **DC blocking and AC coupling in optical communications and high-speed data systems**. Thanks to the Murata's silicon IPD technology, it delivers **low insertion loss, low reflection, and excellent phase stability over an ultra-wide bandwidth from 160 kHz to 160 GHz+**.

Performance

Simulation results of 2x10nF XBDC in transmission mode on microstrip transmission line



4x43Ω single ports to 2x86 Ω differential ports transposition (measurement and simulation)



Disclaimer

This Product Brief is provided for promotional and informational purposes only and does not constitute a Product Datasheet. The information and specifications presented herein are indicative, non-contractual, and may change as the product evolves. This product is under development.

For detailed and up-to-date specifications, including information applicable to the relevant product maturity level, please refer to the latest Product Datasheet. When available, the Product Datasheet is the reference document for exact and contractual product specifications.