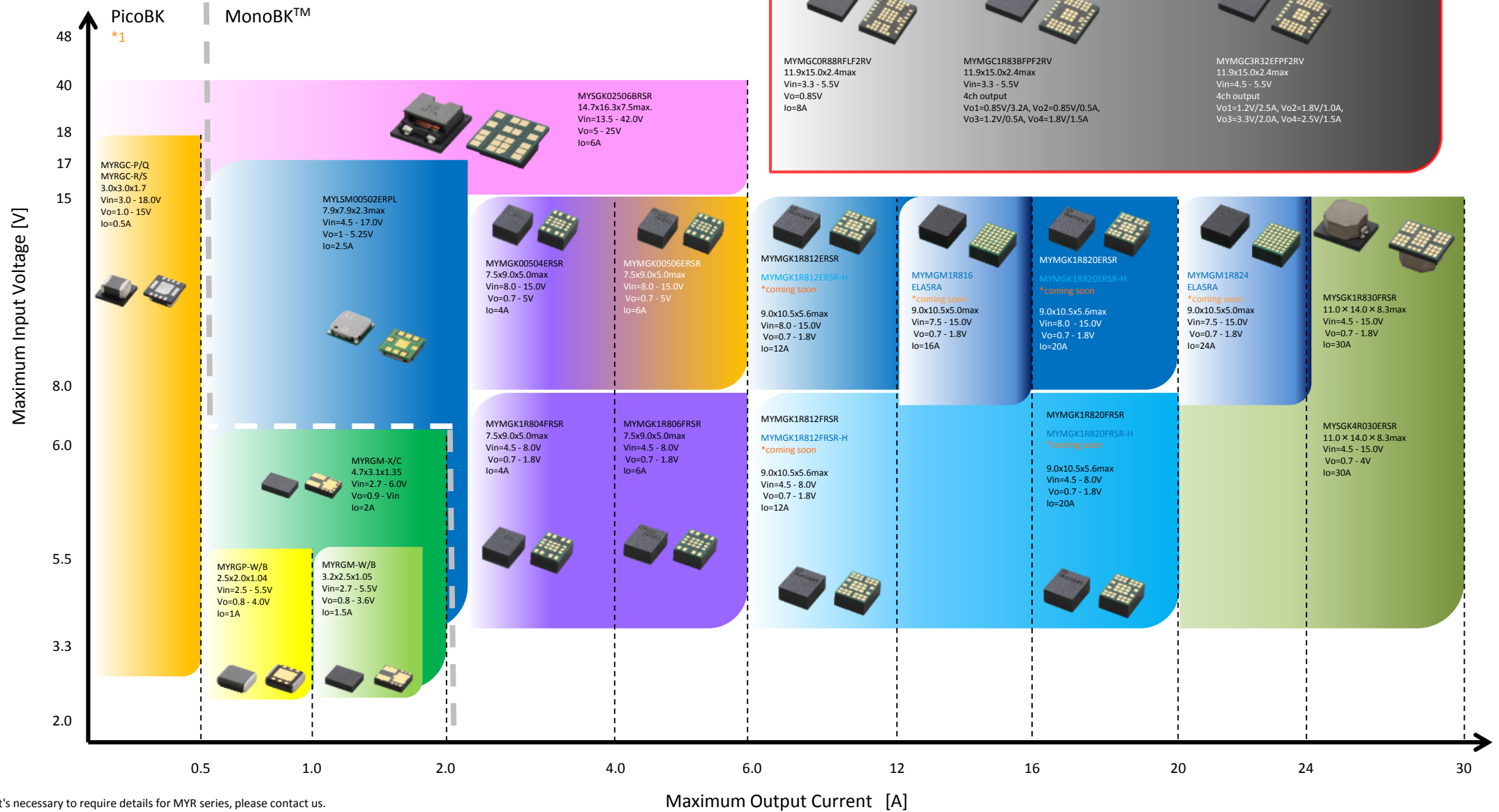


- Standard -



- For FPGA -



*1: If it's necessary to require details for MYR series, please contact us.

*coming soon: Please contact us for product availability and sample lead-time.