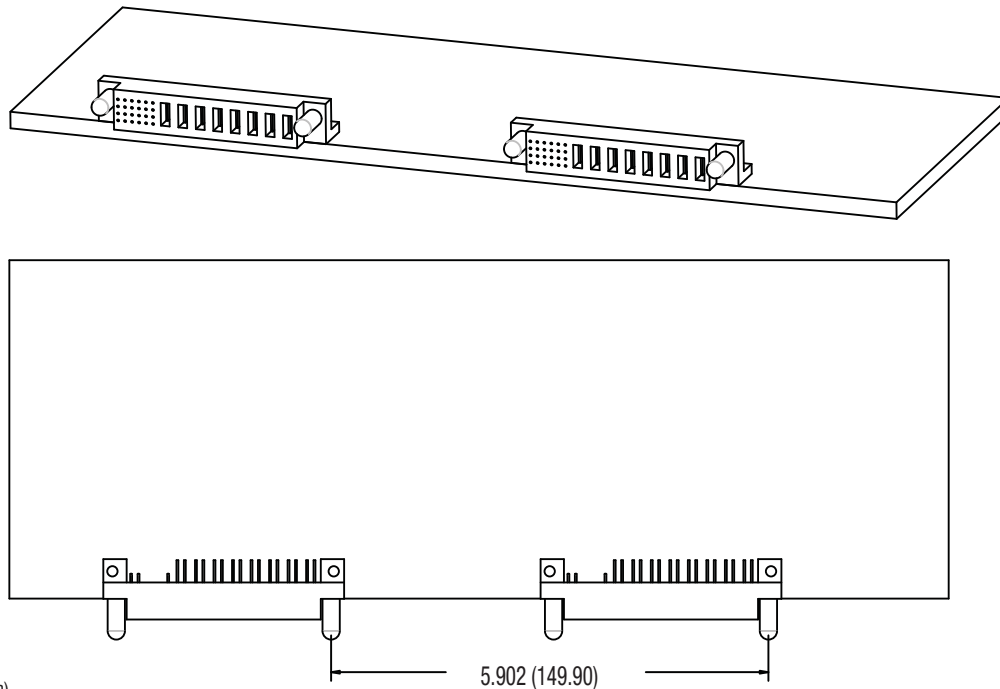


D1U MATING CONNECTORS

	12V D1U mating connector				48V D1U mating connector			
	Press Fit		Solder ¹		Press Fit		Solder ¹	
	Straight	Right Angle	Straight	Right Angle	Straight	Right Angle	Straight	Right Angle
MPS	N/A	N/A	N/A	36-0430032-0	N/A	Pending	N/A	36-0440026-0
FCI	51742-10802400CALF	51762-10802400CBLF	51742-10802400AALF	51762-10802400ABLF	51742-10602000CALF	51762-10602000CBLF	51742-10602000AALF	51762-10602000ABLF

¹ Solder connector recommended for board thickness of <0.090

Recommended layout for 12V D1U right angle connectors

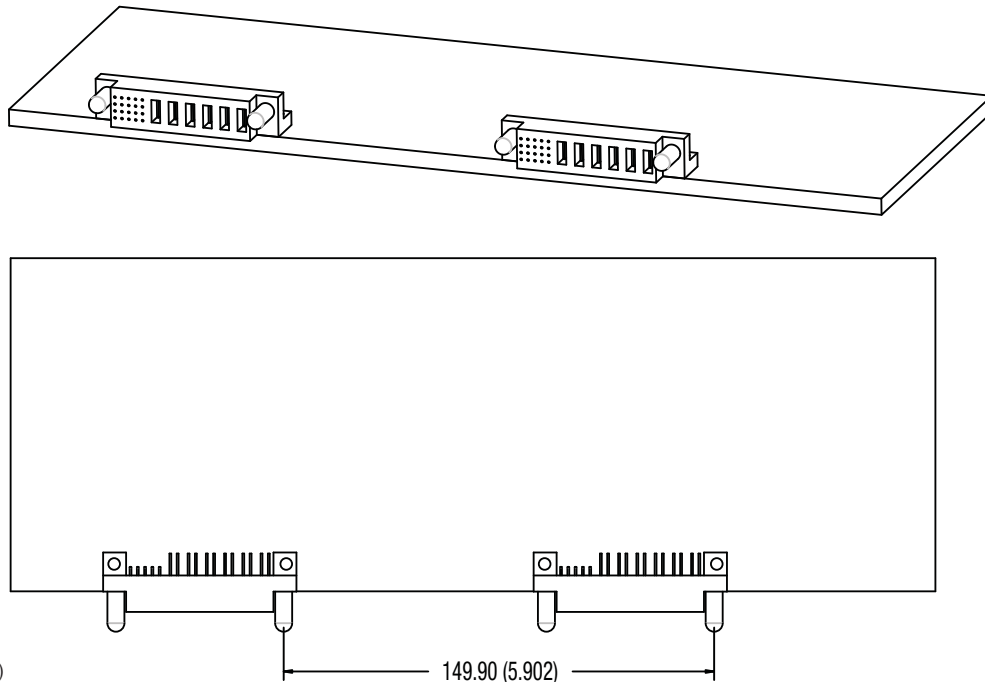


All dimensions in inches (mm)



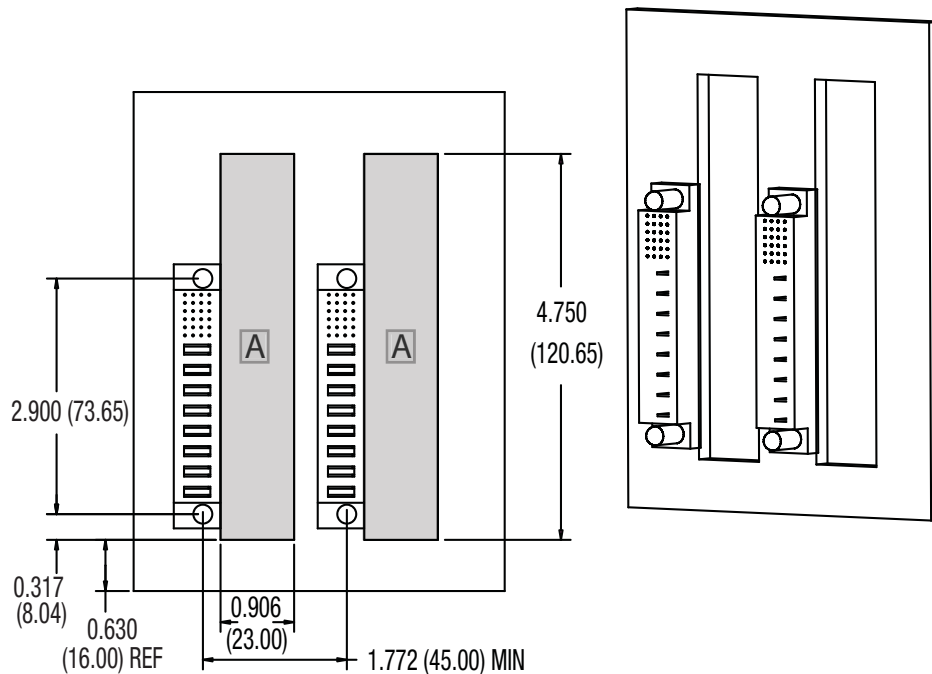
For full details go to
www.murata-ps.com/rohs

Recommended layout for 48V D1U right angle connectors



All dimensions in inches (mm)

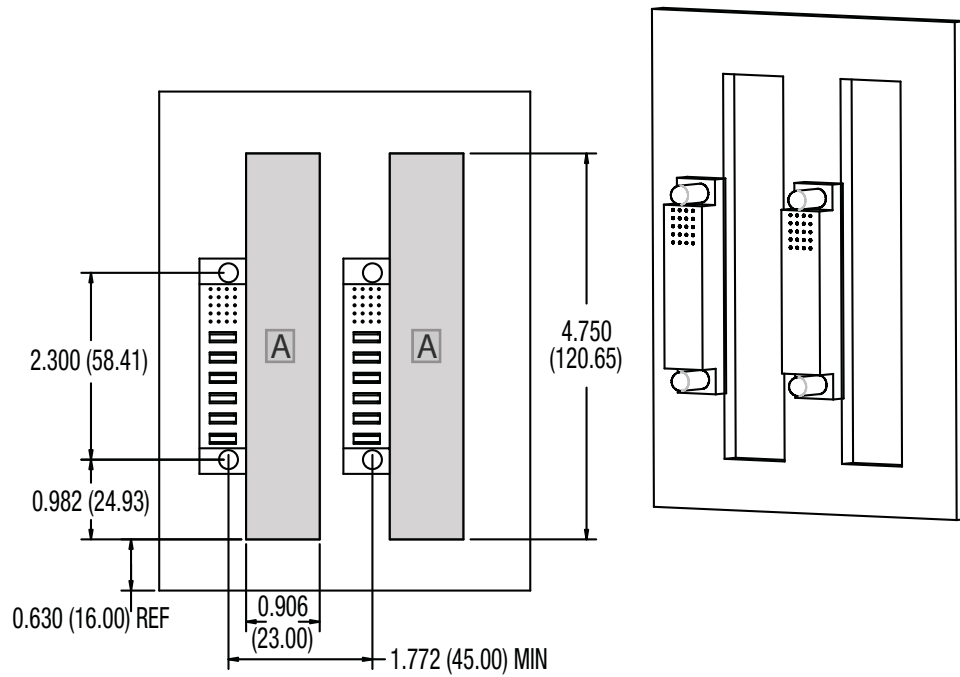
Recommended layout for 12V D1U straight connectors



A 4.75"x0.906" opening for air flow

All dimensions in inches (mm)

Recommended layout for 48V D1U straight connectors



A 4.75"x0.906" opening for air flow

All dimensions in inches (mm)

OUTPUT CONNECTOR AND SIGNAL SPECIFICATION - 12V D1U

DC and Signal Connector: Tyco Part # 1-6450132-2, or FCI PowerBlade # 51732-021

P1	P2	P3	P4	P5	P6	P7	P8	x1	x2	x3	x4	x5	x6	
V _{OUT}	V _{OUT}	V _{RTN}	V _{RTN}	V _{RTN}	V _{RTN}	V _{OUT}	V _{OUT}	AC_OK	P_GOOD	V _{SB} RETURN	V _{SB} RETURN	V _{SB} +OUT	V _{SB} +OUT	D
								SPARE	SPARE	V _{SB} RETURN	V _{SB} RETURN	V _{SB} +OUT	V _{SB} +OUT	C
								I_SHARE	I ² C ADR0	I ² C ADR1	I ² C ADR2	PS_KILL	PS_PRESENT	B
								SENSE +	SENSE -	I ² C DATA	I ² C CLOCK	SPARE	PS_ON	A
mate-last pins														

Pin Assignment	Signal Name	Description	High Level Low Level	I Max
P1, P2, P7, P8	V _{OUT}	Main output voltage		
P3, P4, P5, P6	V _{RTN}	Main output voltage, return		
A1	Sense +	V _{OUT} remote sense, positive node input, connected to the +ve load point		
A2	Sense -	V _{OUT} remote sense, negative node input, connected to the -ve load point		
C5, C6, D5, D6	V _{SB}	Standby voltage output		
C3, C4, D3, D4	V _{SB} Return	Standby voltage, return, tied internally to Output Return		
B1	I_Share	Active load sharing bus	0 – 8V	-4 mA / +5 mA
D1	AC_OK	Input AC Voltage “OK” signal output (Internal pull up is 10kΩ to Vsb)	>2.4V (active, OK) <0.4V	+4 mA -2 mA
D2	P_Good	Power good signal output (Internal pull up is 10kΩ to Vsb)	>2.4V (active, Good) <0.4V	+4 mA -2 mA
B5	PS_Kill	Floating pin will turn off P/S (shorter pin, last-make and first-break contact for hot plugging). This signal overrides PS-On in disabling the Main Output	>2.1V (open, or Vsb) <0.7V (active, PS:On)	N/A
B6	PS_Present	Internally tied to Vsb return	0 V	
A6	PS_On	Internal 1K ohm pull-up to Vsb, (accepts open collector/drain drive), This signal to be pulled low to turn-on power supply	>2.1V (open, or Vsb) <0.7V (active, PS:On)	-4 mA -1 mA
A3	I ² C Data	I ² C serial data bus	Vsb	
A4	I ² C Clock	I ² C serial clock bus	Vsb	
B2	I ² C ADR0	Address input 0, internal pull-up to Vsb	>2.1V, < Vsb <0.8V	±1 mA
B3	I ² C ADR1	Address input 1, internal pull-up to Vsb	>2.1V, <Vsb <0.8V	±1 mA
B4	I ² C ADR2	Address input 2, internal pull-up to Vsb	>2.1V, <Vsb <0.8V	±1 mA

OUTPUT CONNECTOR AND SIGNAL SPECIFICATION - 48V D1U

DC and Signal Connector: Tyco Part # 1-6450332-7, or FCI PowerBlade # 51732-028

P1	P2	P3	P4	P5	P6	x1	x2	x3	x4	x5	
V _{OUT}	V _{OUT}	V _{OUT}	V _{RTN}	V _{RTN}	V _{RTN}	AC_OK	P_GOOD	V _{SB} +OUT	V _{SB} RETURN	V _{SB} RETURN	D
						PS_ON	V _{SB} +OUT	V _{SB} +OUT	V _{SB} RETURN	V _{SB} RETURN	C
						I_SHARE	I ² C ADR0	I ² C ADR1	I ² C ADR2	PS_PRESENT	B
						PS_KILL	V _{OUT} SENSE+	V _{OUT} SENSE-	I ² C DATA	I ² C CLOCK	A

Pin Assignment	Signal Name	Description	High Level Low Level	I Max
P1, P2, P3	V _{OUT}	Main output voltage		
P4, P5, P6	V _{RTN}	Main output voltage, return		
A2	Sense +	V _{OUT} remote sense, positive node input, connected to the +ve load point		
A3	Sense -	V _{OUT} remote sense, negative node input, connected to the -ve load point		
C2, C3, D3	V _{SB}	Standby voltage output		
C4, C5, D4, D5	V _{SB} Return	Standby voltage, return, tied internally to Output Return		
B1	I_Share	Active load sharing bus	0 – 8V	-4 mA / +5 mA
D1	AC_OK	Input AC Voltage “OK” signal output (Internal pull up is 10kΩ to Vsb)	>2.4V (active, OK) <0.4V	+4 mA -2 mA
D2	P_Good	Power good signal output (Internal pull up is 10kΩ to Vsb)	>2.4V (active, Good) <0.4V	+4 mA -2 mA
A1	PS_Kill	Floating pin will turn off P/S (shorter pin, last-make and first-break contact for hot plugging). This signal overrides PS-On in disabling the Main Output	>2.1V (open, or Vsb) <0.7V (active, PS:On)	N/A
B5	PS_Present	Internally tied to Vsb return	0 V	
C1	PS_On	Internal 1K ohm pull-up to Vsb, (accepts open collector/drain drive), This signal to be pulled low to turn-on power supply	>2.1V (open, or Vsb) <0.7V (active, PS:On)	-4 mA -1 mA
A4	I ² C Data	I ² C serial data bus	Vsb	
A5	I ² C Clock	I ² C serial clock bus	Vsb	
B2	I ² C ADR0	Address input 0, internal pull-up to Vsb	>2.1V, <Vsb <0.8V	±1 mA
B3	I ² C ADR1	Address input 1, internal pull-up to Vsb	>2.1V, <Vsb <0.8V	±1 mA
B4	I ² C ADR2	Address input 2, internal pull-up to Vsb	>2.1V, <Vsb <0.8V	±1 mA

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ISO 9001 and 14001 REGISTERED



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