

UHF RFID Tag Data sheet

LXMS21ACNP-184

1. General descriptions

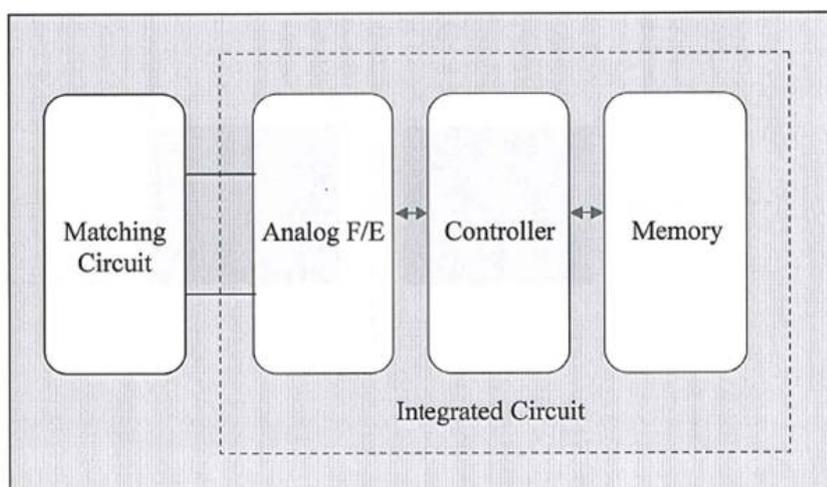
LXMS21ACNP-184 is an innovative RFID module designed to operate in electronic products/applications. It incorporates an industry standard IC.



[Features]

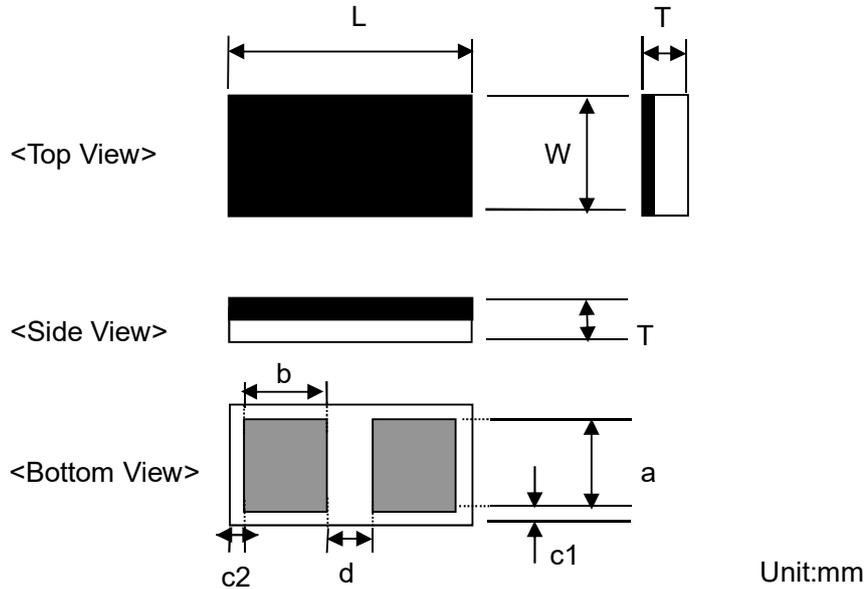
- Small package design
- Reflow SMT compatible
- UHF band (865~928MHz)
- ISO18000-63 / EPC Global Gen2(v1.2.0) Compliant
- Size is 2.0 x 1.2 x 0.5mm
- Using NXP UCODE7xm
- Read range:7m *Reference
- RoHS compliant

2. Block diagram



3. Mechanical information

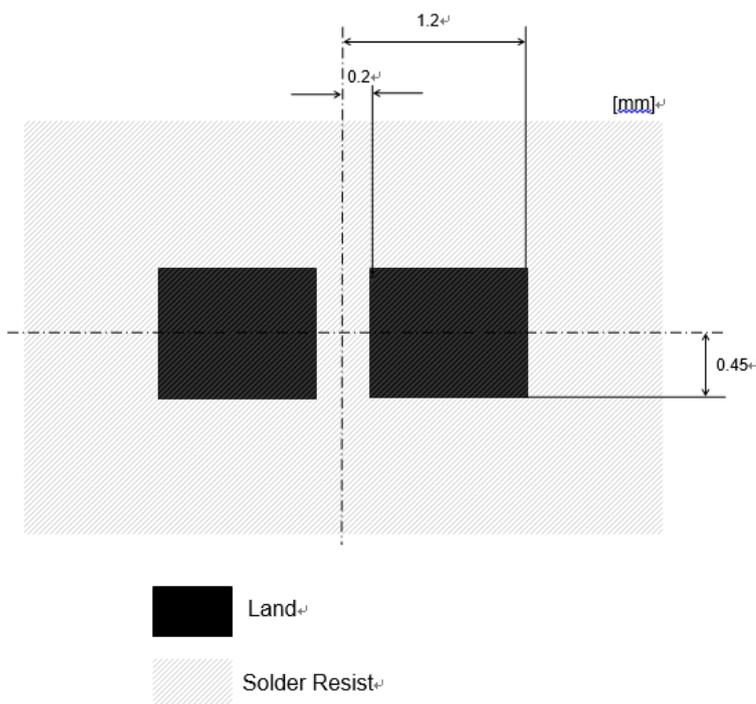
[Dimension]



Unit:mm

Mark	Dimension	Mark	Dimension
L	2.00 ± 0.15	b	0.6 ± 0.1
W	1.20 ± 0.15	c1	0.2 ± 0.2
T	0.5MAX	c2	0.2 ± 0.2
a	0.85 ± 0.10	d	0.4 ± 0.1

[Recommended land pattern]



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4. Electrical performance

4-1. Frequency range

865 – 928MHz

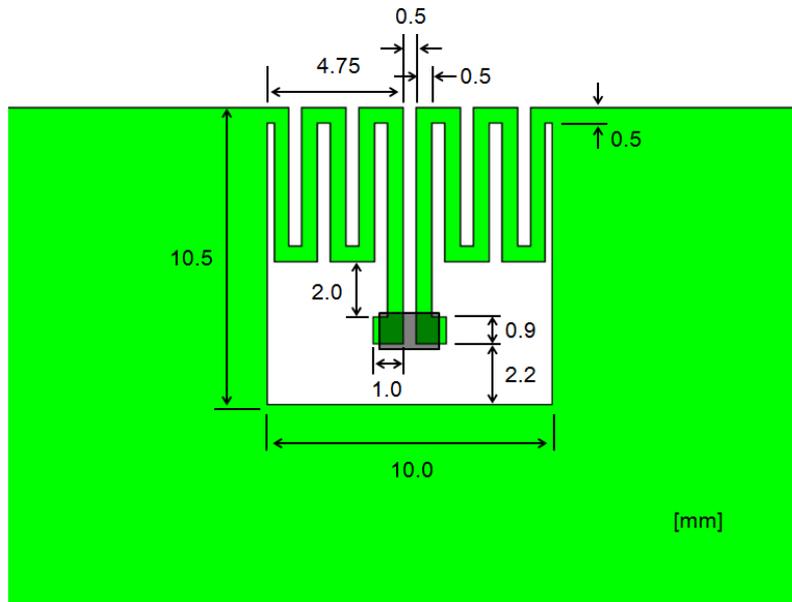
4-2. IC / Memory size

Item		Description	Remarks
IC		NXP UCODE 7xm	
Protocol		ISO/IEC 18000-63	
		EPC global Gen2 V1.2.0	
Memory	EPC	448 bit	Read & Write
	TID	96 bit	Read Only
	Reserved memory	32Bits Access	Read & Write
		32Bits Kill	
User	2046bit		
Data retention Time		※20years	Tamb = 22°C

※Reference value

5. Reference antenna design on PCB

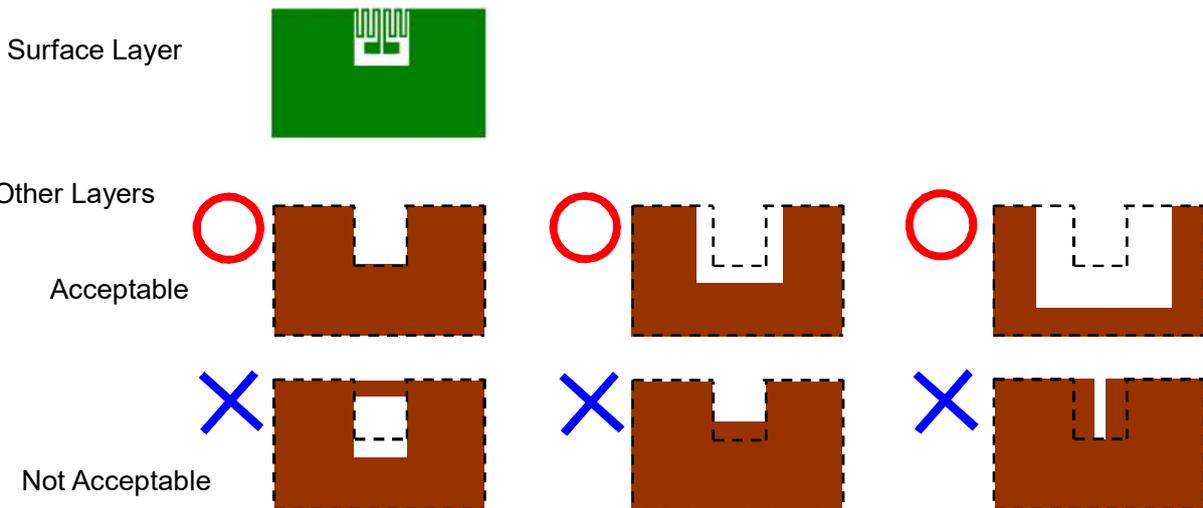
5-1. Top layer



Mounting pattern (normal)
Please conduct to PCB ground

5-2. How to design layers below the mounting pattern

Please remove all the copper pattern area from all layers of the PCB, if it is located under mounting pattern on surface layer.

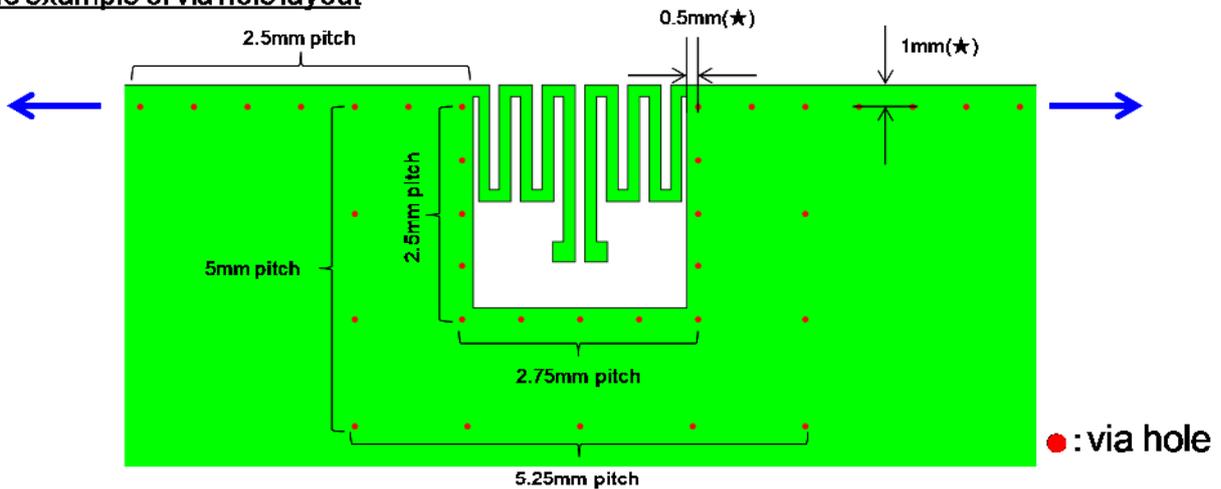


5-3 how to put via hole

Please put via holes as shown in the figure below. If there is no via hole in multi layered PCB, unnecessary inductance degrade performance.

5-3-1 For normal pattern

The example of via hole layout

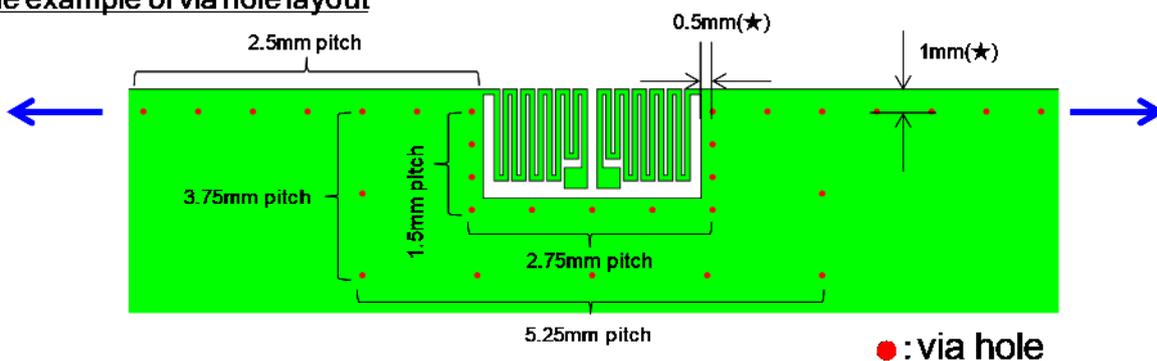


diameter: 0.3mm Φ
pitch : 2.5mm, 2.75mm (around pattern)
5mm, 5.25 mm (other)

At the point of (★), The less value will get the less degradation.
Also increasing the number of via holes will get PCB less degradation.
At blue arrow direction, Please place the via holes to the edge of the board.
(If there is a GND electrode on other layers)

5-3-2 For small pattern

The example of via hole layout



diameter: 0.3mm Φ
pitch : 1.5mm, 2.75mm (around pattern)
3.75, 5.25 mm (other)

At the point of (★), The less value will get the less degradation.
Also increasing the number of via holes will get PCB less degradation.
At blue arrow direction, Please place the via holes to the edge of the board.
(If there is a GND electrode on other layers)

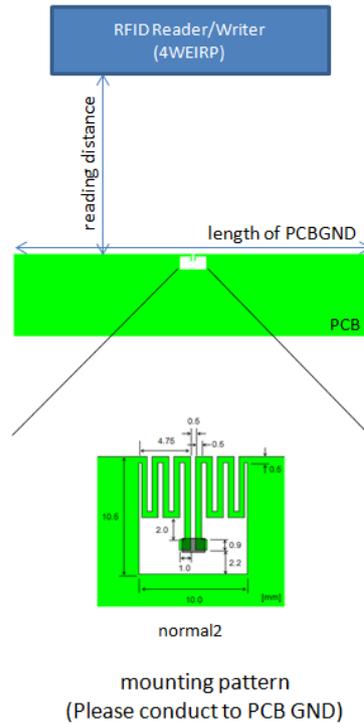
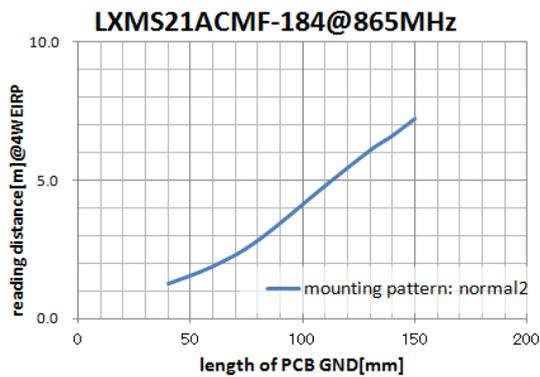
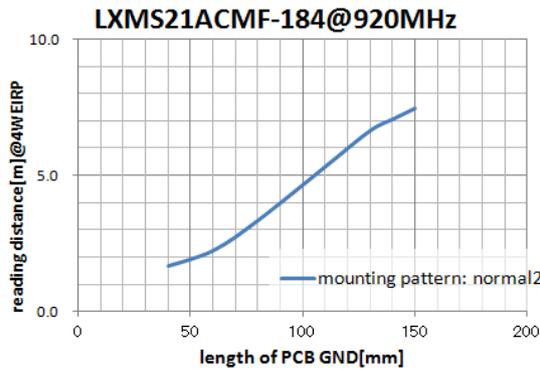
5-4. Reading distance

7m (typ) (at 4wEIRP, with 15cm length of PCB ground)

The reading distance of LXMS21ACNP-184 onto PCB, depends on the length of PCB ground.

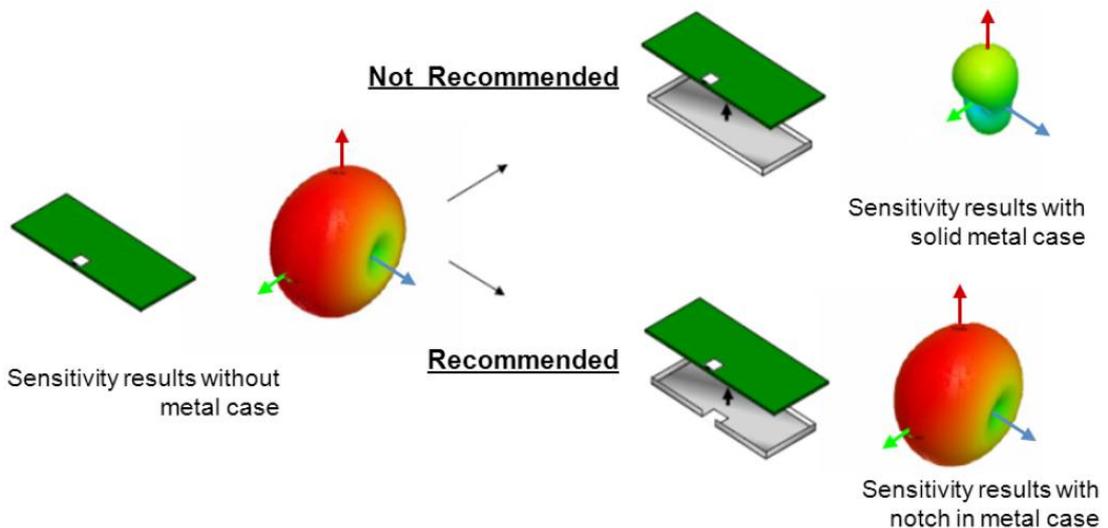
technical information

Reading distance is derived by the parameters of mounting pattern and length of PCB ground.



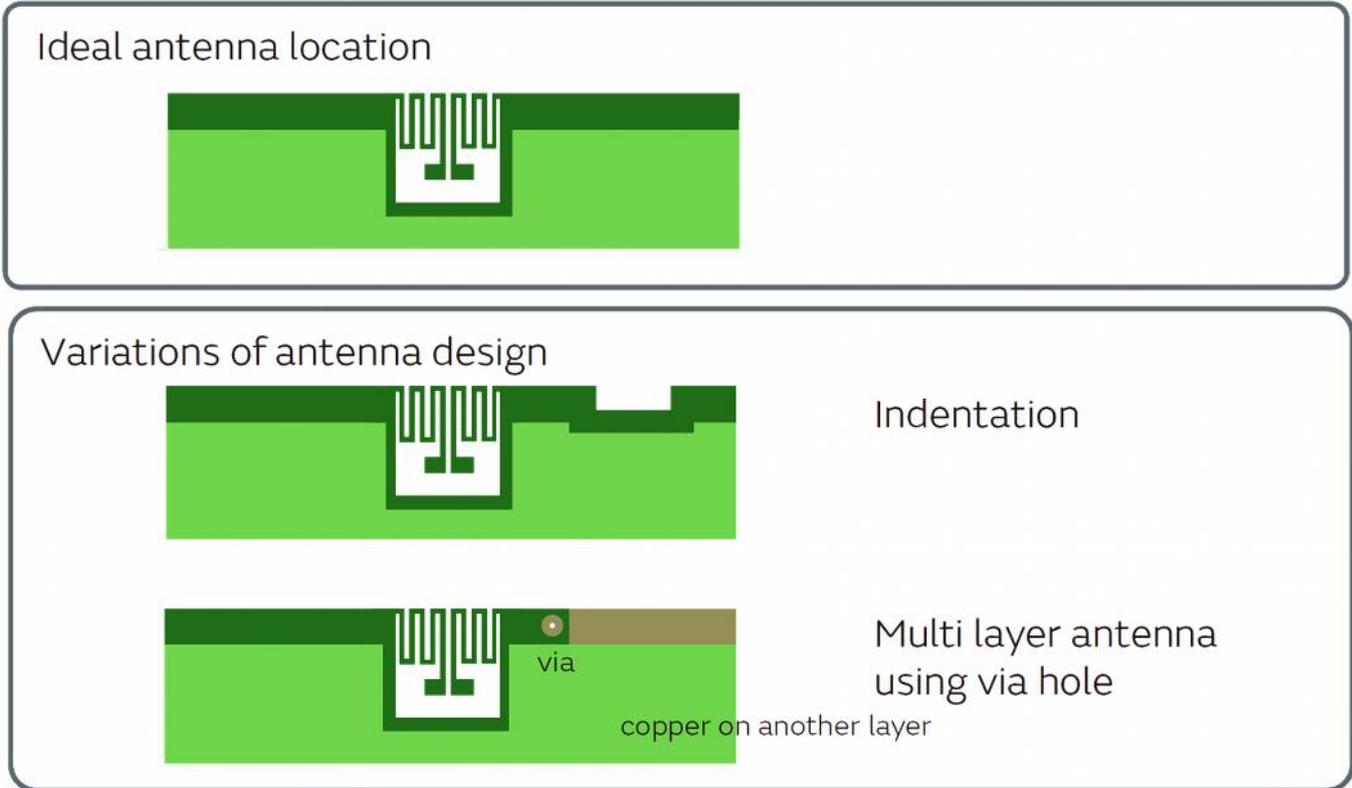
5-5. Metal enclosure under printed circuit board

When the PCB is in close proximity to the metal enclosure, sensitivity is reduced. Removing material directly under the LXMS21ACNP-184 antenna pattern will greatly improve sensitivity.



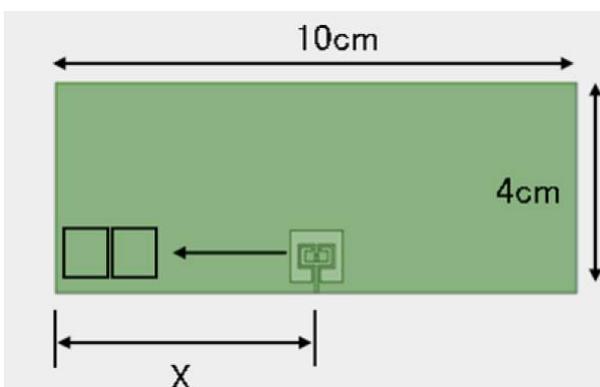
5-6. Antenna location on PCB

Important area of PCB ground is the edge part shown as “dark green” below. The mounting pattern should be located closest to the edge of the PCB. Variations of ideal design are also acceptable.



5-7. Position of LXMS21ACNP-184 on PCB

LXMS21ACNP-184 should be centered on the long side of the PCB to maximize read range. The following illustrates the relationship between “X” length and read range.



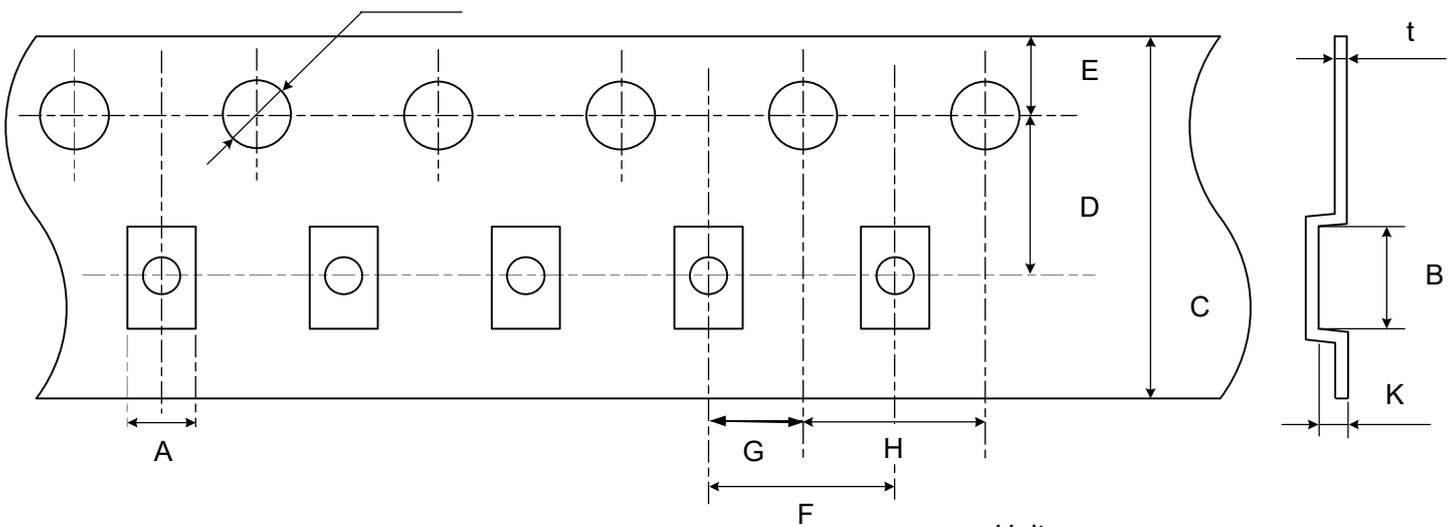
length "X" (cm)	Read Range(m)
5	5
2	3.5
1	2.5

6. Absolute maximum ratings

Symbol	Parameter	Min	Max	Unit
T _{stg}	Storage temperature	-40	+85	°C
T _{amb}	Operating temperature	-40	+85	°C
P _{max}	Total Power Dissipation		100	mW

7. Packaging

7-1. Dimensions of tape

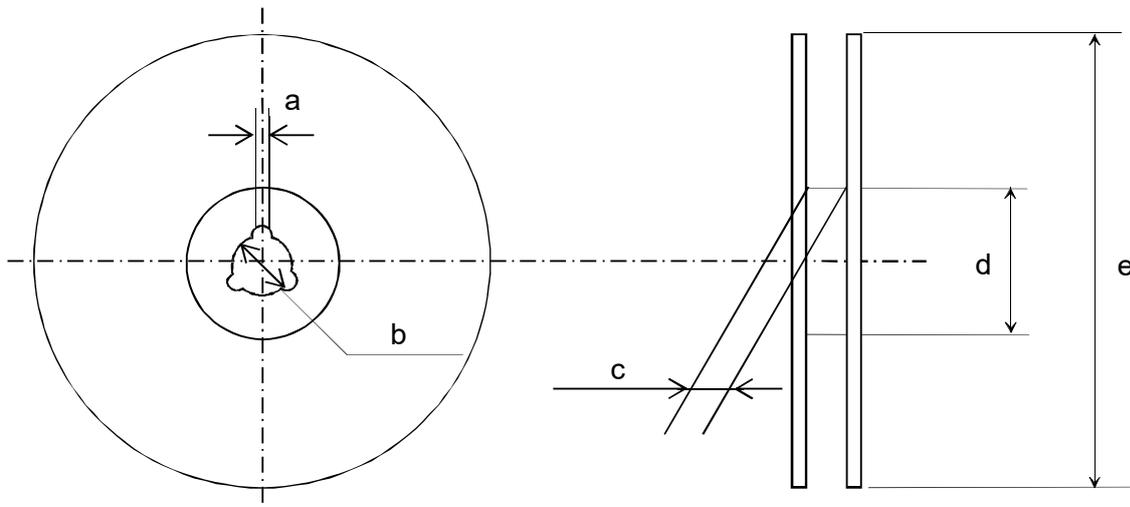


Unit: mm

Symbol	A	B	C	D	E	F
Dimension	1.50±0.1	2.25±0.1	8.0±0.2	3.5±0.05	1.75±0.1	4.0±0.1

Symbol	G	H	J	K	t
Dimension	2.0±0.05	4.0±0.1	1.5±0.1	0.60±0.05	0.25±0.05

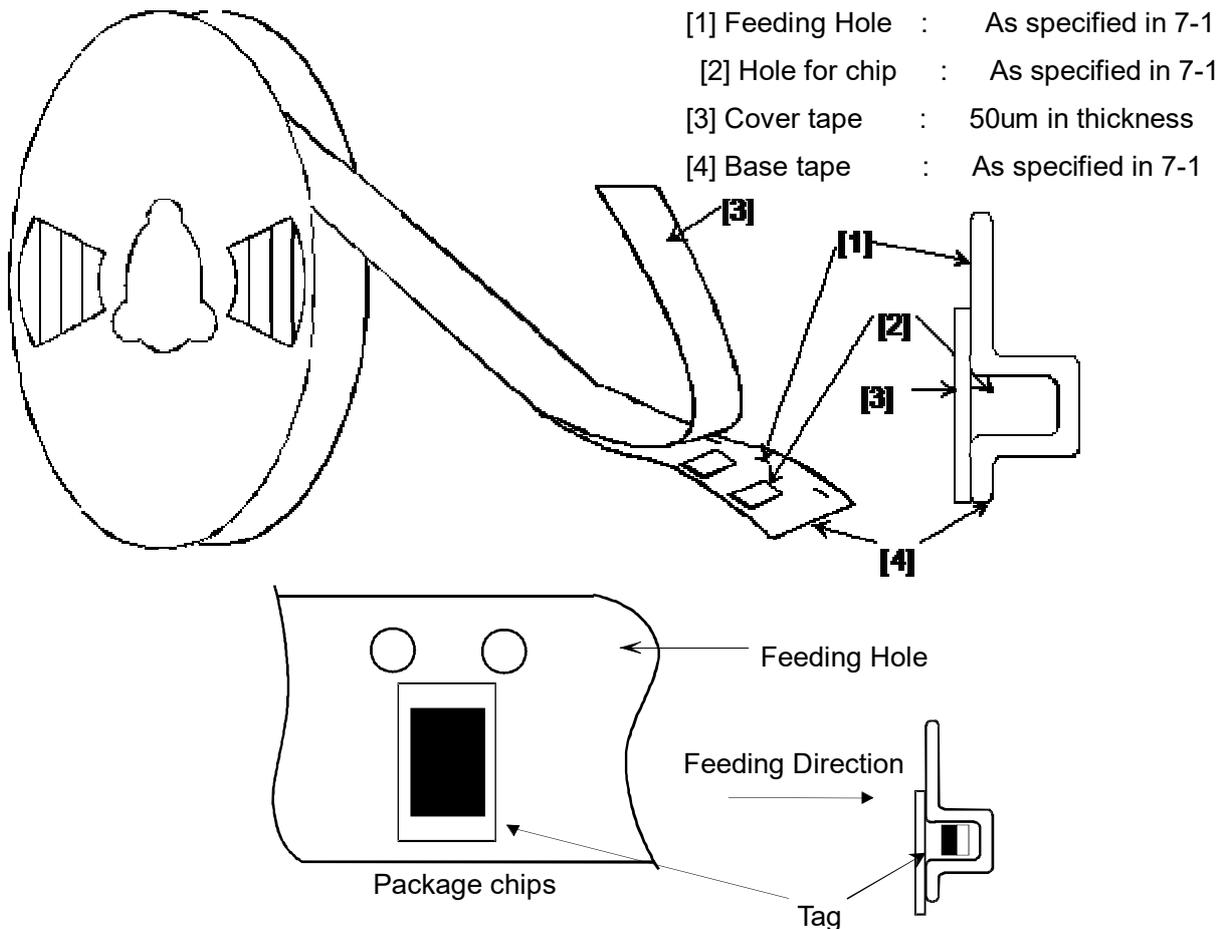
7-2. Dimensions of reel



Unit: mm

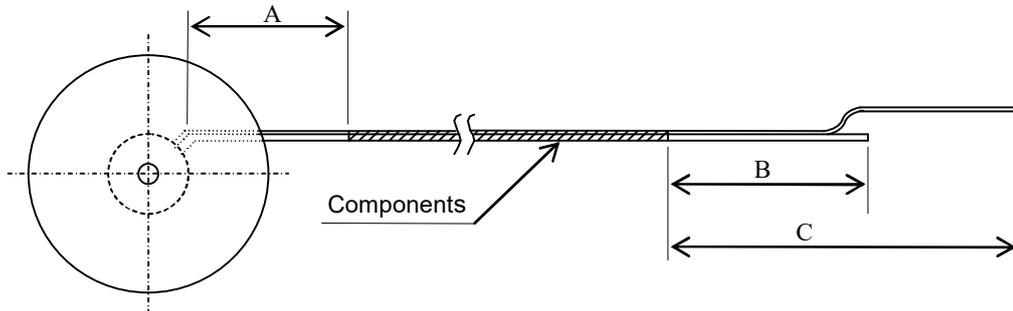
Symbol	a	b	c	d	e
Dimension	2.0+/-0.5	Φ13.0+/-0.2	9.0+/-0.3	(Φ60)	(Φ180)

7-3. Taping Diagrams



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7-4. Leader and Tail tape



Unit : mm

Symbol	Item	Minimum length
A	Tail	160
B	Leader no components	100
C	Leader with cover tape	400

7-5. Taping direction

The tape for chips are wound clockwise.

The feeding holes will come on the right side when the tape is pulled to a user's direction.

7-6. Quantity per reel

5,000 pcs

7-7. Minimum order quantity

5,000pcs

7-8. Material

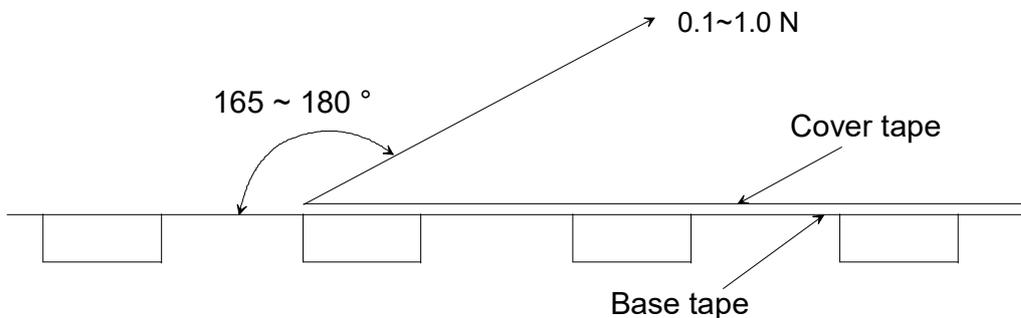
Base and Cover tape : Plastic

Reel : Plastic

Base and Cover tape, Reel have an anti-ESD function.

7-9. Peeling force

0.1~1.0 N in the direction of peeling as shown below.



**UHF RFID Tag Data sheet
LXMS21ACNP-184**



8. Contact window

URL: <http://www.murata.com/products/rfid>

Email: magicstrap@murata.com

For any inquiries/queries, please feel free to contact us.

NOTICE

1. Storage Conditions:

To avoid damaging, be sure to observe the following points.

- Store products where the ambient temperature is 15 to 35 °C and humidity 45 to 75% RH.
(Packing materials, In particular, may be deformed at the temperature over 40 °C.).
- Store products in non corrosive gas (Cl₂, NH₃,SO₂, No_x, etc.).
- Stored products should be used within 6 months of receipt.

2. Handling Conditions:

Be careful in handling or transporting products because excessive stress or mechanical shock may break products.

Handle with care if products may have cracks or damages on their terminals, the characteristics of products may change. Do not touch products with bare hands that may result in poor solderability.

3. Standard PCB Design (Land Pattern and Dimensions):

Please refer to the drawing 1 for the standard land dimensions.

The recommended land pattern and dimensions is as Murata's standard. The characteristics of products may vary depending on the pattern drawing method, grounding method, land dimensions, PCB material, and thickness.

Therefore, be sure to verify the characteristics in the actual system if not using non-standard lands.

4. Notice for Chip Placer:

When placing products on the PCB, products may be stressed and broken by uneven forces from a worn-out chucking locating claw or a suction nozzle. To prevent products from damages, be sure to follow the specifications for the maintenance of the chip placer being used. For the positioning of products on the PCB, be aware that mechanical chucking may damage products.

5. Soldering Conditions:

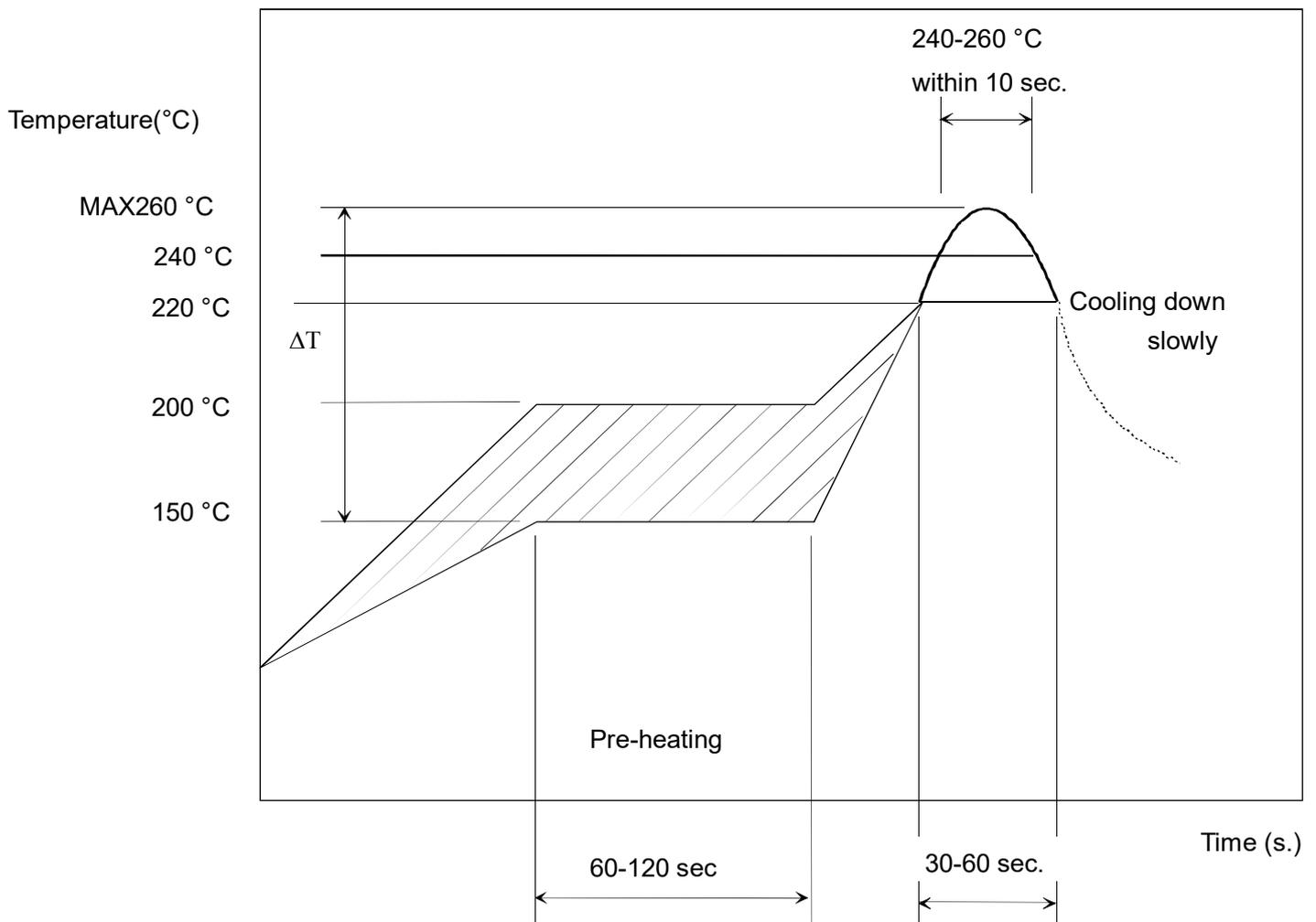
Do not solder the product exceeding two times.

Carefully perform preheating so that the temperature difference (ΔT) between the solder and products surface should be within 130°C.

When products are immersed in solvent after mounting, pay special attention to maintain the temperature difference within 100 °C.

Soldering must be carried out by the above mentioned conditions to prevent products from damage. Contact Murata before use if concerning other soldering conditions.

Reflow soldering standard conditions (Example)



Use rosin type flux or weakly active flux with a chlorine content of 0.2 wt % or less.

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6. Input Power Capacity:

Products shall be used in the input power capacity as specified in this specifications.

Inform Murata beforehand, in case that the components are used beyond such input power capacity range.

7. Cleaning Conditions:

If the cleaning will be applied, please check with Murata in advance since the product may degrade or get broken.

8. Operational Environment Conditions:

Products are designed to work for electronic products under normal environmental conditions (ambient temperature, humidity and pressure). Therefore, products have no problems to be used under the similar conditions to the above-mentioned. However, if products are used under the following circumstances, it may damage products and leakage of electricity and abnormal temperature may occur.

- In an atmosphere containing corrosive gas (Cl₂, NH₃, SO_x, NO_x etc.).
- In an atmosphere containing combustible and volatile gases.
- In a dusty environment.
- Direct sunlight
- Water splashing place.
- Humid place where water condenses.
- In a freezing environment.

If there are possibilities for products to be used under the preceding clause, consult with Murata before actual use.

If static electricity is added to this product, degradation and destruction may be produced.

Please use it after consideration enough so that neither static electricity nor excess voltage is added at the time of an assembly and measurement.

If product malfunctions may result in serious damage, including that to human life, alternative measures of the operation and design must be taken to secure the safety.

9. Limitation of Applications:

Please contact us before using our products for the applications listed below which require especially high reliability for the prevention of defects which might directly cause damage to the third party's life, body or property.

- (1) Aircraft equipment
- (2) Aerospace equipment
- (3) Undersea equipment
- (4) Power plant control equipment
- (5) Medical equipment
- (6) Transportation equipment (vehicles, trains, ships, etc.)
- (7) Traffic signal equipment
- (8) Disaster prevention / crime prevention equipment
- (9) Data-processing equipment
- (10) Application of similar complexity and/or reliability requirements to the application listed in the above.



Note:

Please make sure that your product has been evaluated and confirmed against your specifications when our product is mounted to your product.

All the items and parameters in this product specification have been prescribed on the premise that our product is used for the purpose, under the condition and in the environment agreed upon between you and us. You are requested not to use our product deviating from such agreement.