### Timing device

#### Block Diagram

**Block Diagram for Surround View Camera System**

**Camera Module**
- C-MOS/CCD Image Sensor
- AD Converter
- Oscillator
- DA Converter

**Main Control Module**
- AD Converter
- ISP
- Image Processor

**Case 1**
- Oscillator
- Analog Signal

**Case 2**
- LVDS
- PLC FPD-Link Camera -Link

**Case 3**
- Ethernet®

#### Appearance

<table>
<thead>
<tr>
<th>Appearance</th>
<th>Type</th>
<th>Series P/N</th>
<th>SPEC</th>
</tr>
</thead>
<tbody>
<tr>
<td><img src="image1.png" alt="Image" /></td>
<td>Ceramic Resonator</td>
<td><strong>CSTNR-G-C</strong> series (4MHz to 7.99MHz), <strong>CSTNE-G-C</strong> series (8MHz to 13.99MHz), <strong>CSTNE-V-C</strong> series (14MHz to 20MHz)</td>
<td>Frequency Tolerance : +/-2700ppm</td>
</tr>
<tr>
<td><img src="image2.png" alt="Image" /></td>
<td>Crystal Unit</td>
<td>Representative P/N XRCGB25M000F3A00R0 (for Ethernet®) \ XRCGB27M000F3A00R0 (for Camera ISP)</td>
<td>Frequency Tolerance : +/-85ppm</td>
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</tbody>
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