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http://www.murata.com/en-sg/products/productdetail?partno=CSTNE16M0V53Z000R0

# CSTNE16M0V53Z000R0









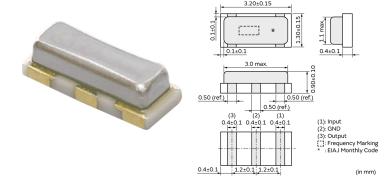


## **Applications**

Unsuitable	Please be sure to read and comply with	
Applications	these "Precautions for use."	
Applications  Specific Applications	. ,	
	management, and safety required for the above applications, and use	
	Products after confirming the	
	performance and reliability of the actual	
	'	
	Product.	



## Appearance & Shape





## Packaging Information

Packaging	Specifications	Standard Packing Quantity
R0	180mm Embossed Tape	3000



### **Features**

MURATA's package technology expertise has enabled the development of the Chip CERALOCK.

High-density mounting is made possible by the small package and the elimination of the need for an external load capacitor.

### Features

- 1. Oscillation circuits do not require external load capacitors.
- 2. Available for a wide frequency range.
- 3. Extremely small and have a low profile.
- 4. No adjustment is necessary for oscillation circuits.
- 5. Available for a wide temperature range.
- 6. Stable supply is ensured due to not using precious metal(Palladium).

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2. This datasheet has only typical specifications because there is no space for detailed specifications.

Therefore, please review our product specifications or consult the approval sheet for product specifications before ordering





### Product Search Data Sheet

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# **Specifications**

Draduct Type	Ceramic Resonator
Product Type	(CERALOCK)
Series	CSTNE_V
Frequency	16.000MHz
Frequency Tolerance	+/-0.50% max.
Operating Temperature Range	-40°C~125°C
Frequency Shift by	
Temperature	+/-0.30% max.
Frequency Aging	+/-0.20% max.
Resonant Impedance (R1)	40ohm max.
Built-in Load Capacitance (CL1/CL2)	15pF
Shape	SMD
Wash	Not available
L x W (size)	3.2x1.3mm
Mass	16.972mg

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