

Product Search Data Sheet

Note: This datasheet may be out of date.

Please download the latest datasheet of CFWLA455KDFA-B0 from the official website of Murata Manufacturing Co., Ltd.

http://www.murata.com/en-us/products/productdetail?partno=CFWLA455KDFA-B0

CFWLA455KDFA-B0









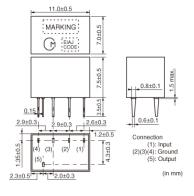
Applications

Unsuitable	Please be sure to read and comply with	
Applications	these "Precautions for use."	
Specific Applications	Consumer equipment	
	Please refer to Our Website and	
	specifications, etc. for information about	
	the performance, functions, quality,	
	management, and safety required for	
	the above applications, and use	
	Products after confirming the	
	performance and reliability of the actual	
	Product.	



Appearance & Shape







Packaging Information

Packaging	Specifications	Standard Packing Quantity
-B0	Bulk	150



Features

Ceramic filter CFWLAseries are low profile high selectivity ceramic filters, which use 6 elements in ladder form. They are best suitable to high-class transceivers, cordless telephones and amateur radios.

Features

- 1. Low profile, high selectivity
- 2. Available bandwidths are B to J as standard
- 3. Easily mountable on any PC board
- 4. Operating temperature range: -20 to +80 (degrees C), Storage temperature range: -40 to +85 (degrees C)

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Attention

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2. This datasheet has only typical specifications because there is no space for detailed specifications.

Therefore, please review our product specifications or consult the approval sheet for product specifications before ordering





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Specifications

Operating Temperature Range	-20°C to 80°C	
Shape	Lead	
Elements	6	
Center Frequency	455.0kHz	
Nominal Center Value	Yes	
6dB Bandwidth	fn±10.0kHzmin.	
Stop Bandwidth	20.0kHz	
Area of Stop Bandwidth	[within 50dB]	
Stop Band Attenuation	35dB[within fn±100kHz]	
Insertion Loss	4.0dB[at minimum loss point]	
Ripple	3.0dB[within fn±7kHz]	
Input/Output Impedance	1500Ω	
Mass	690mg	

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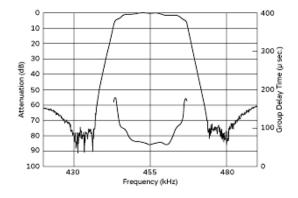


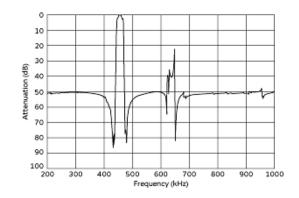
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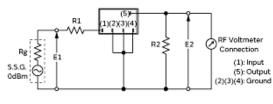






Frequency Characteristics

Spurious Response



Rg+R1=R2=Input/Output Impedance

Measurement Circuit

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