

Product Search Data Sheet

Note: This datasheet may be out of date.

Please download the latest datasheet of CFULA455KG1Y-B0 from the official website of Murata Manufacturing Co., Ltd.

http://www.murata.com/en/products/productdetail?partno=CFULA455KG1Y-B0

CFULA455KG1Y-B0









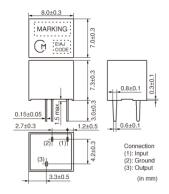
Applications

Unsuitable	Please be sure to read and comply with	
Applications	these "Precautions for use."	
Specific Applications	Consumer equipment	
	Please refer to Our Website and	
	specifications, etc. for information about	
	the performance, functions, quality,	
	management, and safety required for	
	the above applications, and use	
	Products after confirming the	
	performance and reliability of the actual	
	Product.	



Appearance & Shape







Packaging Information

Packaging	Specifications	Standard Packing Quantity
-B0	Bulk	200



Features

CFULA_Y series are high selectivity ceramic filters, which consist of 4 ceramic elements connected in a ladder form.

They are most suitable for digital communications and cellular phones because of their improved GDT characteristics.

Features

- 1. High selectivity
- 2. Avariety of bandwidths are available.
- 3. Excellent GDT characteristics are available within pass bandwidth
- 4. Easily mounted on a printed circuit board
- 5. Operating temperature range: -20 to +80 (degrees C), Storage temperature range: -40 to +85 (degrees C)

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Attention

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 $2. This \ data{sheet has only typical specifications because there is no \ space for \ detailed \ specifications.}$

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ATOR IN ELECTRONICS

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Specifications

Operating Temperature Range	-20°C to 80°C	
Shape	Lead	
Elements	4	
Center Frequency	455.0kHz	
Center Frequency Tolerance	±1kHz	
Nominal Center Value	No	
6dB Bandwidth	fn±4.5kHzmin.	
Stop Bandwidth	15.0kHz	
Area of Stop Bandwidth	[within 40dB]	
Stop Band Attenuation	23dB[within fn±100kHz]	
Insertion Loss	10.0dB[at minimum loss point]	
Ripple	1.0dB[within fn±3kHz]	
GDT Deviation	20.0µs[within fn±3kHz]	
Input/Output Impedance	2000Ω	
Mass	1018mg	

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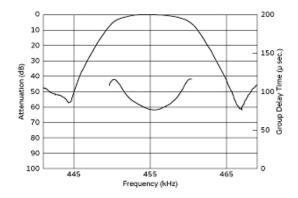
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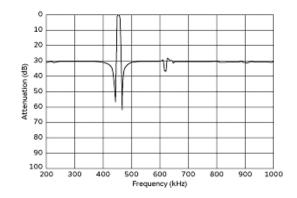
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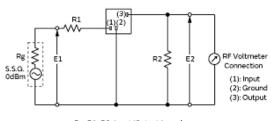
Product Data





Frequency Characteristics





Rg+R1=R2=Input/Output Impedance

Measurement Circuit

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