

FEATURES

- Industry standard footprint
- 2:1 input range, 3W output power, minimum
 3:1 input range with power derating to 2W
- Dual isolated output
- Short circuit protection
- Operating temperature range -40°C to 85°C
- Load and line regulation <1% on both outputs
- No heatsink required
- 1kVDC isolation 'Hi Pot Test'
- 5V, 12V & 24V inputs
- 12V & 15V outputs
- Internal SMD construction
- Fully encapsulated

DESCRIPTION

The NDTD series is a range of low profile DC-DC converters offering dual outputs over a 2:1 input voltage range. All parts deliver 3W output power up to 85°C without heatsinking. A flyback oscillator design with isolated feedback is used to give regulation over the full operating range of 25% to 100% of full load. It is strongly recommended that external capacitors be used on input and output to guarantee performance over full load and input voltage range (see application notes for guidance). The plastic case is rated to UL94V-0 and encapsulant to UL94V-1 and the connection pins are formed from a tin plated alloy 42 leadframe.





ND ⁻	TD	Ser	ies

Isolated 3W Wide Input Dual Output DC-DC Converters

SELECTION	GUIDE								
	ut ige	output ìge	Output	current	ut ent pad	incy ² 1.)	tion tance		
Order code	Input voltage	Rated output voltage	Min. Load ¹	Full load	Input current full load	Efficiency ² (Min.)	lsolation capacitance	MTTF ³	Recommended Alternative
	V (Nom.)	V	mA	mA	mA	%	pF	kHrs	
Recommended In Production									
NDTD0512C	5	±12	±31	±125	764	76	36	1625	
NDTD1212C	12	±12	±31	±125	311	78	32	1650	
NDTD1215C	12	±15	±25	±100	310	78	36	1633	
NDTD2412C	24	±12	±31	±125	148	82	35	2075	
NDTD2415C	24	±15	±25	±100	146	82	41	2080	
				Discon	tinued				
NDTD0503C	5	±3.3	±113	±454	890	67	30	1644	Contact Murata
NDTD0505C	5	±5	±75	±300	804	72	31	1640	Contact Murata
NDTD0515C	5	±15	±25	±100	773	75	34	1609	Contact Murata
NDTD1203C	12	±3.3	±113	±454	343	73	30	1668	Contact Murata
NDTD1205C	12	±5	±75	±300	321	75	29	1665	Contact Murata
NDTD2403C	24	±3.3	±113	±454	170	73	30	1671	Contact Murata
NDTD2405C	24	±5	±75	±300	156	78	30	1675	Contact Murata
NDTD4803C	48	±3.3	±113	±454	86	72	30	1667	Contact Murata
NDTD4805C	48	±5	±75	±300	79	76	30	1669	Contact Murata
NDTD4812C	48	±12	±31	±125	76	80	35	2090	Contact Murata
NDTD4815C	48	±15	±25	±100	75	81	36	2045	Contact Murata

INPUT CHARACTERISTICS						
Parameter	Conditions	Min.	Тур.	Max.	Units	
Voltage range	5V input types	4.5	5	9		
	12V input types	9	12	18	VDC	
	24V input types	18	24	36		
	48V input types	36	48	75		
Reflected ripple current ⁴	5V input types		40	90	mA p-p	
	12V, 24V & 48V input types		30	40		

GENERAL CHARACTERISTICS							
Parameter	Conditions	Min.	Тур.	Max.	Units		
Switching frequency	100% to 25% load, VIN min to max	60		620	kHz		

1. Please refer to minimum load application note section on page 4.

2. Measured at full load with external input/output capacitors, refer to test circuit.

3. Calculated using MIL-HDBK-217F with nominal input voltage at full load (ground benign) at 25°C.

4. Please refer to reflected ripple current measurement circuit on page 3.

All specifications typical at T_A=25°C, nominal input voltage and rated output current unless otherwise specified.

NDTD Series

Parameter	Conditions	Conditions		Тур.	Max.	Units
Voltage set point accuracy		5V outputs		±2	±5	0/
	With external input/output capacitors	12V & 15V outputs		±1	±3	%
Line regulation	Low line to high line with external input/output capa	citors		0.15	0.5	%
Load regulation 25% load to 100% load with excapacitors with balanced load	25% load to 100% load with external input/output	5V outputs		0.5	1.0	%
	capacitors with balanced load	12V & 15V outputs		0.2	0.5	%
Ripple	BW = 20Hz to 300kHz with external input/output ca	BW = 20Hz to 300kHz with external input/output capacitors		15	40	mV rms
Ripple & noise	BW = DC to 20MHz with external input/output capac	itors		90	150	mV p-p
Cross regulation	% voltage change on negative output when positive load varies from 12% to 50% of 3W rating,	NDTD05XXC, NDTD1205C, NDTD2405C, NDTD4805C			5.0	%
	with negative load fixed at 50%	NDTD1212C, NDTD1215C, NDTD2412C, NDTD2415C, NDTD4812C, NDTD4815C		2.1	3.0	
Short circuit protection				Cont	nuous	

ISOLATION CHARACTERISTICS							
Parameter	Conditions	Min.	Тур.	Max.	Units		
Isolation voltage	Flash tested for 1 second	1000			VDC		
Resistance	Viso=1KVDC	1			GΩ		

TEMPERATURE CHARACTERISTICS						
Parameter	Conditions	Min.	Тур.	Max.	Units	
Operation		-40		85		
Storage		-50		130		
0	NDTD0505C, NDTD0512C, NDTD1205C, NDTD2405C, NDTD4805C		35		°C	
Case temperature rise above ambient in still air	NDTD0515C, NDTD1212C, NDTD1215C, NDTD2412C, NDTD4812C, NDTD2415C, NDTD4812C		28			

ABSOLUTE MAXIMUM RATINGS	
Short-circuit protection	8 hours
Lead temperature 1.5mm from case for 10 seconds	260°C
Minimum output load for specification ¹	25% of rated output
Wave Solder	Wave Solder profile not to exceed the profile recommended in IEC 61760-1 Section 6.1.3. Please refer to <u>application notes</u> for further information.
Input voltage 5V types	10V
Input voltage 12V types	20V
Input voltage 24V types	40V
Input voltage 48V types	80V
Free air space	10mm Min. around component

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TECHNICAL NOTES

ISOLATION VOLTAGE

'Hi Pot Test', 'Flash Tested', 'Withstand Voltage', 'Proof Voltage', 'Dielectric Withstand Voltage' & 'Isolation Test Voltage' are all terms that relate to the same thing, a test voltage, applied for a specified time, across a component designed to provide electrical isolation, to verify the integrity of that isolation.

Murata Power Solutions NDTD series of DC-DC converters are all 100% production tested at their stated isolation voltage. This is 1kVDC for 1 second.

A question commonly asked is, "What is the continuous voltage that can be applied across the part in normal operation?"

For a part holding no specific agency approvals, such as the NDTD series, both input and output should normally be maintained within SELV limits i.e. less than 42.4V peak, or 60VDC. The isolation test voltage represents a measure of immunity to transient voltages and the part should never be used as an element of a safety isolation system. The part could be expected to function correctly with several hundred volts offset applied continuously across the isolation barrier; but then the circuitry on both sides of the barrier must be regarded as operating at an unsafe voltage and further isolation/insulation systems must form a barrier between these circuits and any user-accessible circuitry according to safety standard requirements.

REPEATED HIGH-VOLTAGE ISOLATION TESTING

It is well known that repeated high-voltage isolation testing of a barrier component can actually degrade isolation capability, to a lesser or greater degree depending on materials, construction and environment. The NDTD series has an El ferrite core, with no additional insulation between primary and secondary windings of enamelled wire. While parts can be expected to withstand several times the stated test voltage, the isolation capability does depend on the wire insulation. Any material, including this enamel (typically polyurethane) is susceptible to eventual chemical degradation when subject to very high applied voltages thus implying that the number of tests should be strictly limited. We therefore strongly advise against repeated high voltage isolation testing, but if it is absolutely required, that the voltage be reduced by 20% from specified test voltage.

This consideration equally applies to agency recognised parts rated for better than functional isolation where the wire enamel insulation is always supplemented by a further insulation system of physical spacing or barriers.

RoHS COMPLIANCE INFORMATION



This series is compatible with RoHS soldering systems with a peak wave solder temperature of 260°C for 10 seconds. Please refer to <u>application notes</u> for further information. The pin termination finish on this product series is Tin. The series is backward compatible with Sn/Pb soldering systems.

For further information, please visit www.murata.com/en-global/products/power/rohs

PART NUMBER STRUCTURE NDTD XX XX C Series name Input voltage PART NUMBER STRUCTURE

NDTD Series

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TERMINOLOGY

LINE REGULATION

The percentage change in output voltage between low intput voltage and high intput voltage, measured with fixed output load i.e. a 5V part with an output voltage of 5.05V @ high input voltage and 5.03V @ low input voltage would have a line regulation of 0.4%.

Line regulation = Vout (Low Input V) - Vout (High Input V) x100%

Vout (Nominal Input V)

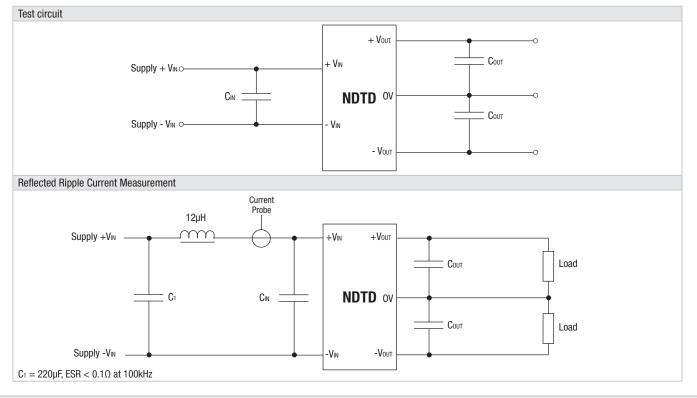
Where VOUT (Nominal Input V) is 5V.

APPLICATION NOTES

Recommended Input & Output Capacitors

Although these converters will work without external capacitors, they are necessary in order to guarantee the full parametric performance over the full line and load range. All parts have been tested and characterised using the following values and test circuit.

Input Voltage	Cin	Output Voltage	Соит
5V, 12V	100μF, 25V (0.25Ω at 100kHz)	5V	100μF, 25V (0.25Ω at 100kHz)
24V, 48V	10μF, 100V (1.5Ω at 100kHz)	12V, 15V	47μF, 25V (0.4Ω at 100kHz)



Cross Regulation

Load regulation is at its best when the positive and negative loads are balanced. When the loads are asymmetric, the negative output is not as tightly regulated as the positive output. To meet ripple specification a total minimum load of 25% full load is required, however, the NDTD can be used with much lighter loading at the expense of increased ripple. A small load is required on the negative output of 150mW to ensure the maximum negative output voltage is not exceeded.

NDTD Series

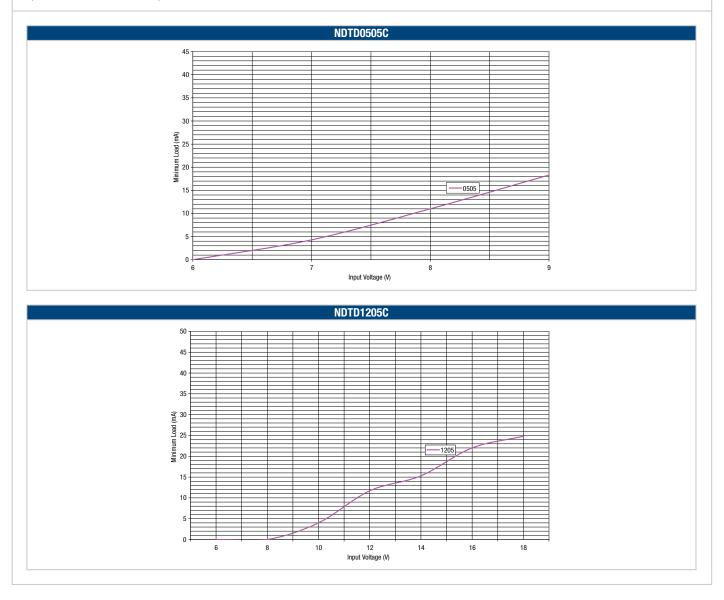
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APPLICATION NOTES (Continued)

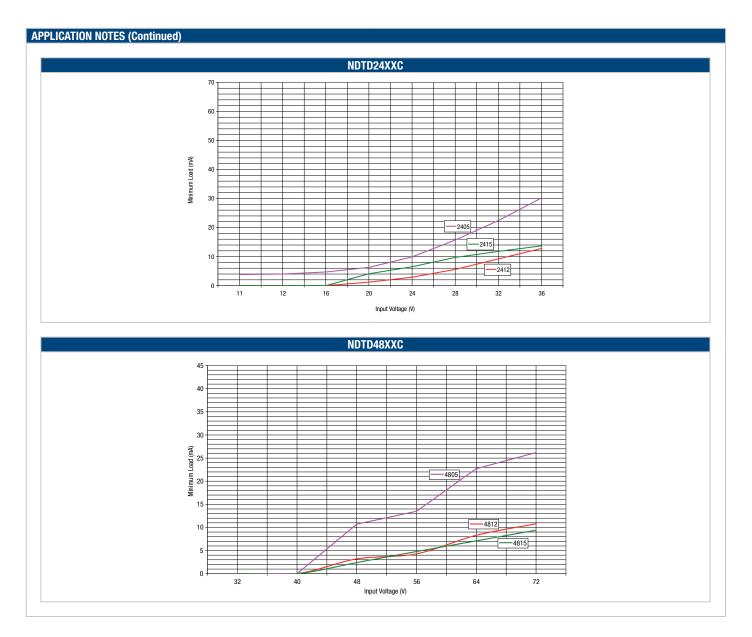
Minimum Load

The minimum load for correct operation is 25% of the full rated load across the specified input voltage range. Lower loads may cause a significant increase in output ripple and may cause the output voltage to exceed its specification transiently during power-down when the input voltage also falls below its rated minimum.

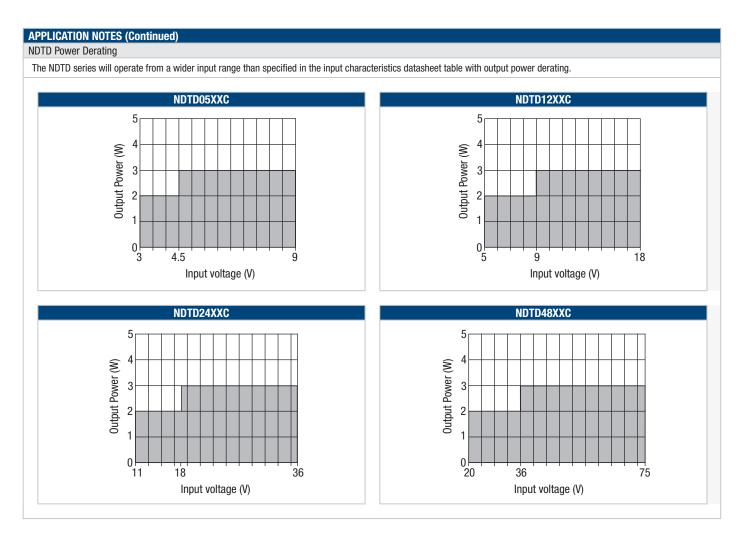
The following graphs show the typical required minimum load required for stable operation in mA verses input voltage. Some variants are not included as they do not typically require a minimum load for stable operation: NDTD0512C, NDTD0515C, NDTD1212C and NDTD1215C.



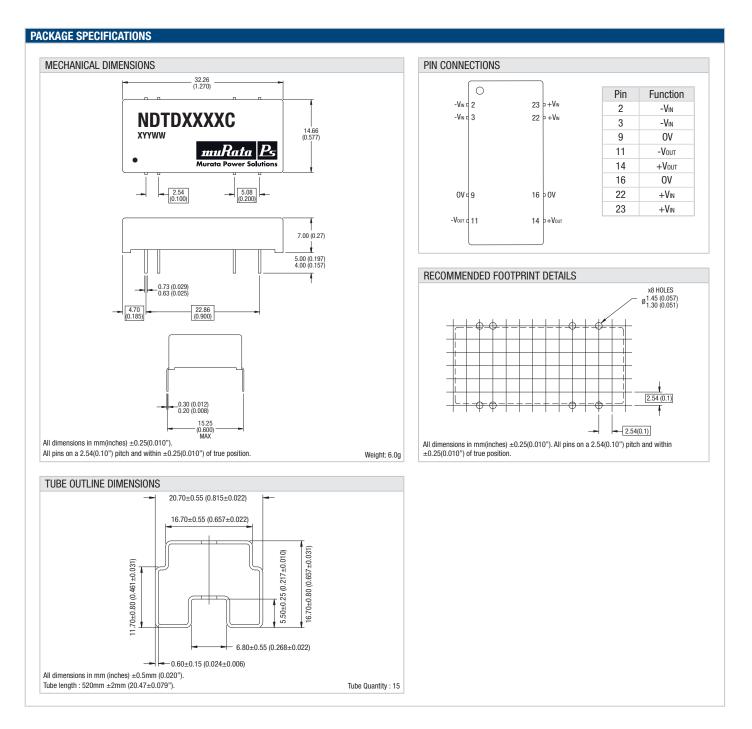
NDTD Series



NDTD Series



NDTD Series



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